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PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

3092

APPL NUM 10021259	FILING DATE 12/19/2001	CLASS 438	SUBCLASS 5	GAU 2829	EXAMINER Kilday
**APPLICANTS: Hasegawa Hiroyuki; Yamaoka Totonori; Ishihara Yoshio; Masusaki Hiroshi; Satou Takayuki; Suzuki Katsumasa; Tokunaga Hiroki;					
**CONTINUING DATA VERIFIED: THIS APPLICATION IS A DIV OF 09/651,255 08/30/2000					
** FOREIGN APPLICATIONS VERIFIED: JAPAN 11-246240 08/31/1999 JAPAN P2000-052555 02/28/2000 JAPAN P2000-052516 02/28/2000					
PG-PUB <input type="checkbox"/>		DO NOT PUBLISH <input type="checkbox"/>		RESCIND <input type="checkbox"/>	
Foreign priority claimed <input type="checkbox"/> yes <input type="checkbox"/> no 35 USC 119 conditions met <input type="checkbox"/> yes <input type="checkbox"/> no Verified and Acknowledged Examiners's initials				ATTORNEY DOCKET NO P 284101 OSP-11726	
TITLE : Method of purging CVD apparatus and method for judging maintenance of times of semiconductor production apparatuses					

U.S. DEPT. OF COMM./PAT. & TM-PTO-436L (Rev. 12-94)

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NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Total Claims Print Claim for O.G.	
ISSUE FEE		DRAWING	
Amount Due	Date Paid	Sheets Drwg.	Figs. Drwg. Print Fig.
<input type="checkbox"/> TERMINAL DISCLAIMER		Primary Examiner PREPARED FOR ISSUE Application Examiner	
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